



i2c packet

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Personal digital assistant (PDA) based I2C bus analysis

JW Bruce, MA Gray, RF Follett - Consumer Electronics, IEEE Transactions on, 2003 -
 ieeexplore.ieee.org

... I2CLib_Recv(UINT16 refNum, UINT8 *addr, UINT8 *data, UINT8 *len) This function receives an entire I2C packet from the software read FIFO. ...

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System Architecture Directions for Networked Sensors - group of 94 »

J Hill, R Szewczyk, A Woo, S Hollar, D Culler, K ... - portal.acm.org

... 0 AM temperature 78 32 AM light 146 8 AM 356 40 Packet 334 40 RADIO byte 810 8 RFM 310 1 Photo 84 1 Temperature 64 1 UART 196 1 UART packet 314 40 I2C bus 198 ...

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WISENET

D PATNODE, DR SCHERTZ - cegt201.bradley.edu

... The main flow of WiseDB was to wait until a packet was ... Initialize Hardware Activate Sensors Acquire Sensor Data Format Data Packet Transmit Data Packet ...

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Shift invariant wavelet packet bases - group of 4 »

I Cohen, S Raz, D Malah - Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., ..., 1995 - ieeexplore.ieee.org

... [10] I. Cohen, S. Raz and D. Malah, "Orthonormal shift- invariant wavelet packet decomposition and representation ... i2c) (21) (ln,s (l'-l.Zn+l.,n+li 3t) ig. ...

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Two-level contention resolution scheme for WDM networks with control channels - group of 2 »

DS Kim, CK Un - Electronics Letters, 1993 - ieeexplore.ieee.org

... Fig. 4 OC-I2c transmitter and receiver traces Receiver ... cycles. The length of a data packet is equal to that of a cycle (= N minislots). Each ...

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Design and implementation of a sensor network node for ubiquitous computing environment - group of 3 »

Y Kawahara, M Minami, H Morikawa, T Aoyama - Vehicular Technology Conference, 2003. VTC 2003-Fall. 2003 ..., 2003 - ieeexplore.ieee.org

... void to_app_layer(packet_type* packet); // send data to application CPU via I2C void from_app_layer(packet_type* packet); // receive data from application CPU ...

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PRODUCTS

L PACKET, SM DC, DC CONVERTERS - IEEE CIRCUITS & DEVICES MAGAZINE, 2003 - ieeexplore.ieee.org

... protection, the SR70 is ideal for use in such high-speed signal interface applications as ADSL, USB1.1/2.0 ports, I2C,microprocessor buses, video ... LEGEND PACKET ...

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J Hill, R Szewczyk, A Woo, S Hollar, D Culler, K ... - Architectural Support for Programming Languages and ..., 2000 - cse.unsw.edu.au

... <http://tinyos.millennium.berkeley.edu> Composition into a Complete Application RFM

Radio byte Radio Packet i2c Temp photo Messaging Layer clocks bit byte packet ...

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THE EMERALD PROTOCOL SUITE: DESIGN AND IMPLEMENTATION OF A MODULAR, DISTRIBUTED

B Palmintier - 2004 - hubbard.engr.scu.edu

... Figure 11: Basic I2C packet format. ... 34 xvii Page 18. Figure 14: The Status Packet

Structure consists of a single byte enclosed in an I2C packet..35 ...

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Readout of the CMS Pixel Detector. - group of 3 »

D Kotlinski, R Baur, K Gabathuler, R Horisberger, ... - Proc. Electronics for LHC Experiments (Cracow 2000) - cms.web.psi.ch

... A packet includes a header and trailer provided by the TBM chip. ... Both chips, the ROC and the TBM will include I2C interfaces in order to be able to program ...

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J Hill, R Szewczyk, A Woo, S Holler, D Culler, K ... - portal.acm.org

... TX RX I2C SPI AT 90LS8535 32.768 MHz clock ... Any sensors in range of this broadcast record the identity of the base station and then **rebroadcast** out the update. ...Cited by 935 - Web Search - BL Direct**From motes to java stamps: smart sensor network testbeds - group of 4 »**

TC Henderson, JC Park, N Smith, R Wright - Intelligent Robots and Systems, 2003.(IROS 2003)., 2003 - ieeexplore.ieee.org

... configuration SandR {} implementation (cluster (or **rebroadcast** cluster) ... Other sensor options include Dallas 1-Wire, I2C and SPI devices. ...Cited by 5 - Web Search**ReWINS: a distributed multi-RF sensor control network for industrial automation - group of 2 »**

H Ramamurthy, D Lal, BS Prabhu, R Gadh - Wireless Telecommunications Symposium, 2005, 2005 - wireless.ucla.edu

... IEEE 1451) use serial channels like I2C, microwire, field bus for communication and thus in our design the hardware peripherals have been ... **rebroadcast** the packet ...View as HTML - Web Search**Tangible Toolkits for Reflective Systems Modeling - group of 3 »**

TM Gorton - 2003 - timgorton.com

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Submitted to the Department of Electrical Engineering ...

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E Aarts, R Collier, E Van Loenen - 2003 - books.google.com

... It also provides a wide range of input output interfaces: USB, I2C, PCMCIA, 1 15 Kbits IRDA, ISO 7816-2 Smart- card, 8 bit audio CODEC, graphical LCD ...

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D Type, C Workpackages - ist-phenix.org

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A 64-min single-chip voice recorder/player using embedded 4-b/cell flash memory - group of 4 »

M Borgatti, A Rocchi, M Bisio, M Besana, L Navoni, ... - Solid-State Circuits, IEEE Journal of, 2001 - ieeexplore.ieee.org

... A master/slave I2C interface has been added to the MCU to provide a direct link to ... in a standard 3-V 0.5-mm common-ground NOR embedded flash EEPROM process (two ...
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Z8 Encore!® Flash MCU - group of 2 »

MS Timer - dkc3.digikey.com

... Features: • Program Memory Type: Flash • On-Chip Special Features: POR, VBO • WDT • COMM Interfaces: I2C, UART, OCD • Speed (20MHz) • Oper. ...
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Leveraging Boundary Scan resources for comprehensive cluster testing - group of 4 »

H Ehrenberg, N Muench, GE LLC - Proceedings of Board Test Workshop 2004 - tmsinc.co.kr

... 3.3.2 EEPROM FLASH devices and other EEPROM (such as serial EEPROM based on I2C or SPI protocol) can be programmed via Boundary Scan devices if access is ...
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